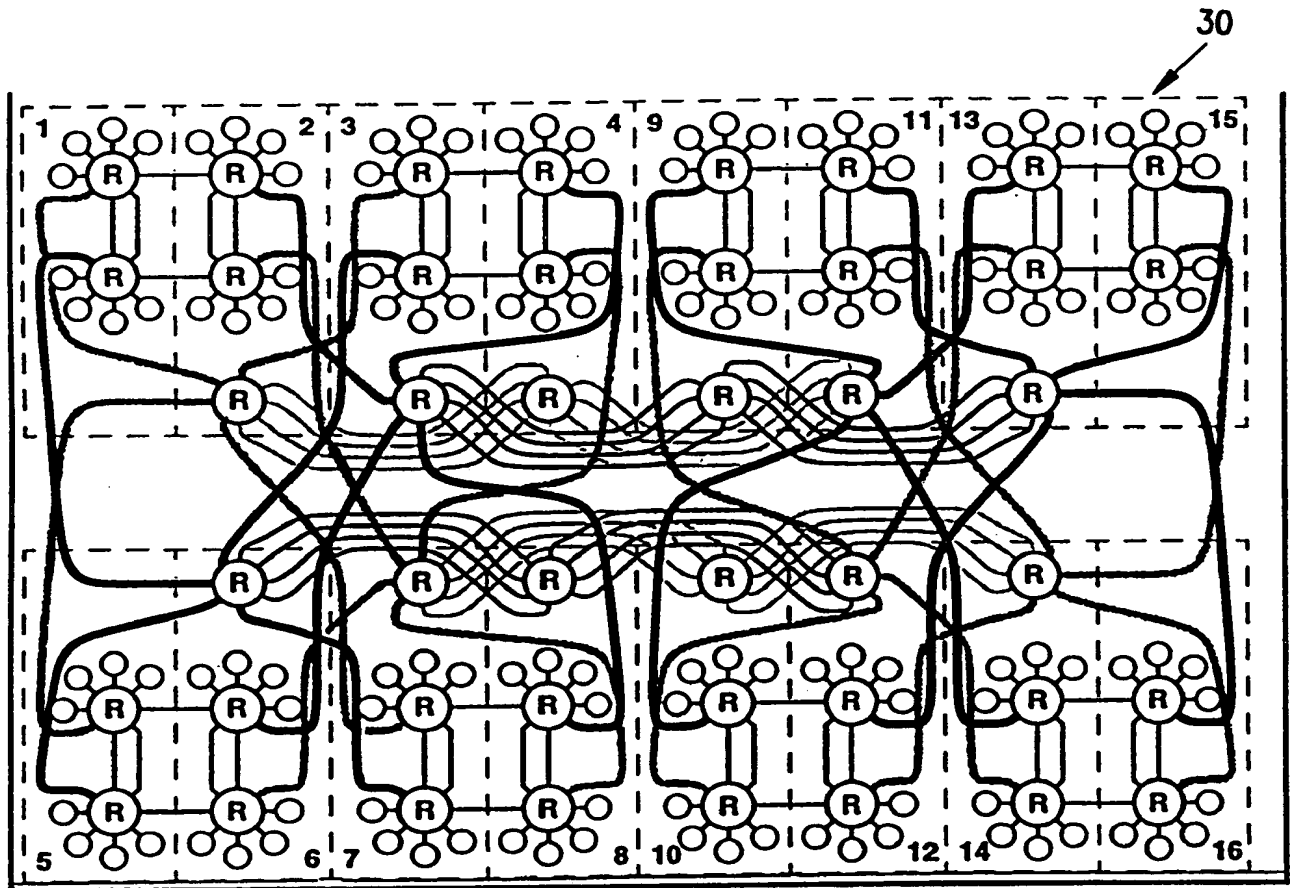


FIG. 1A



512 CPU

EXTENDED HYPERCUBE TOPOLOGY

FIG. 1B

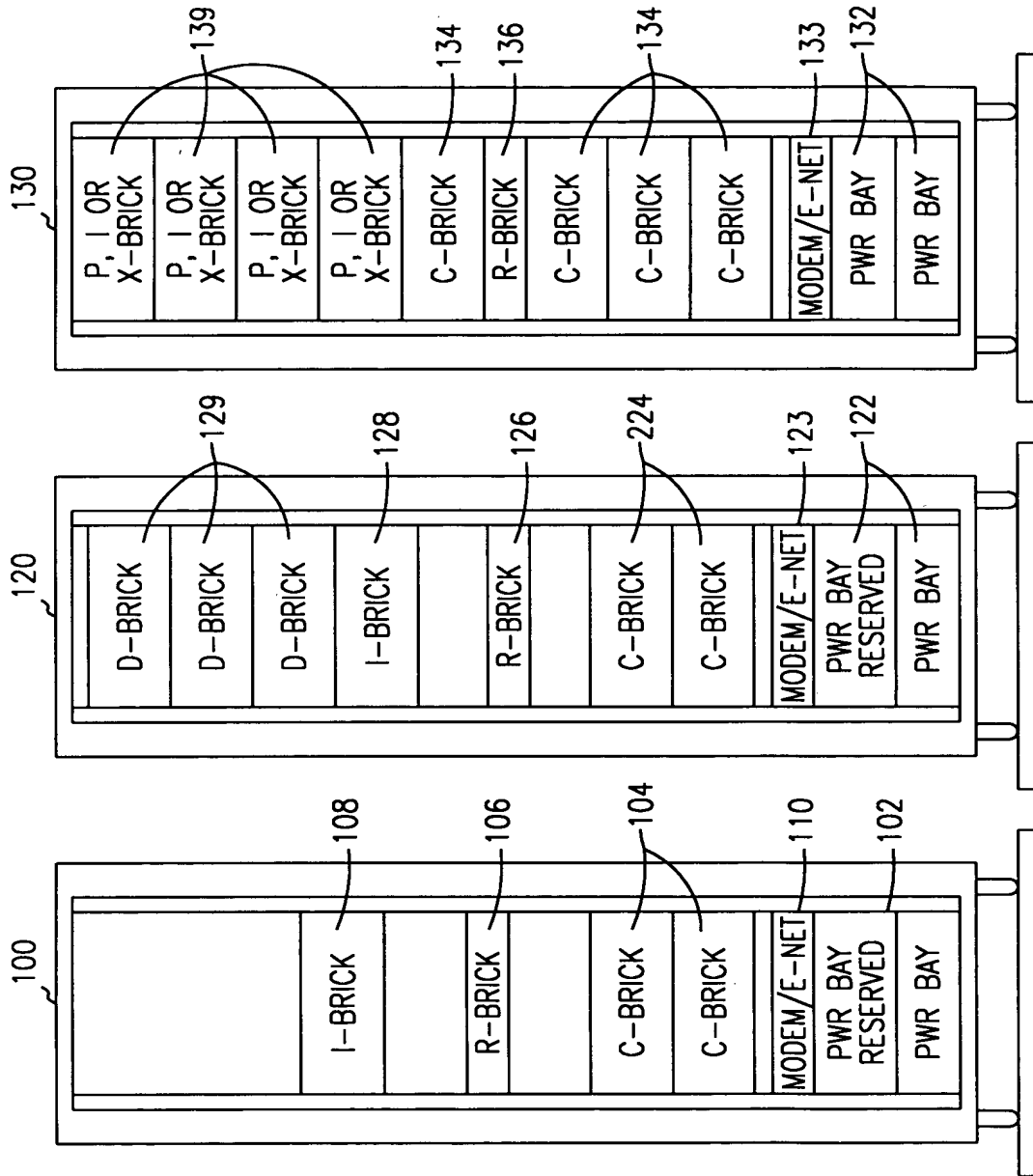


FIG. 1C





TITLE: MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE
INVENTOR NAME: Martin M. Deneroff et al.
SERIAL NO.: 09/408,874

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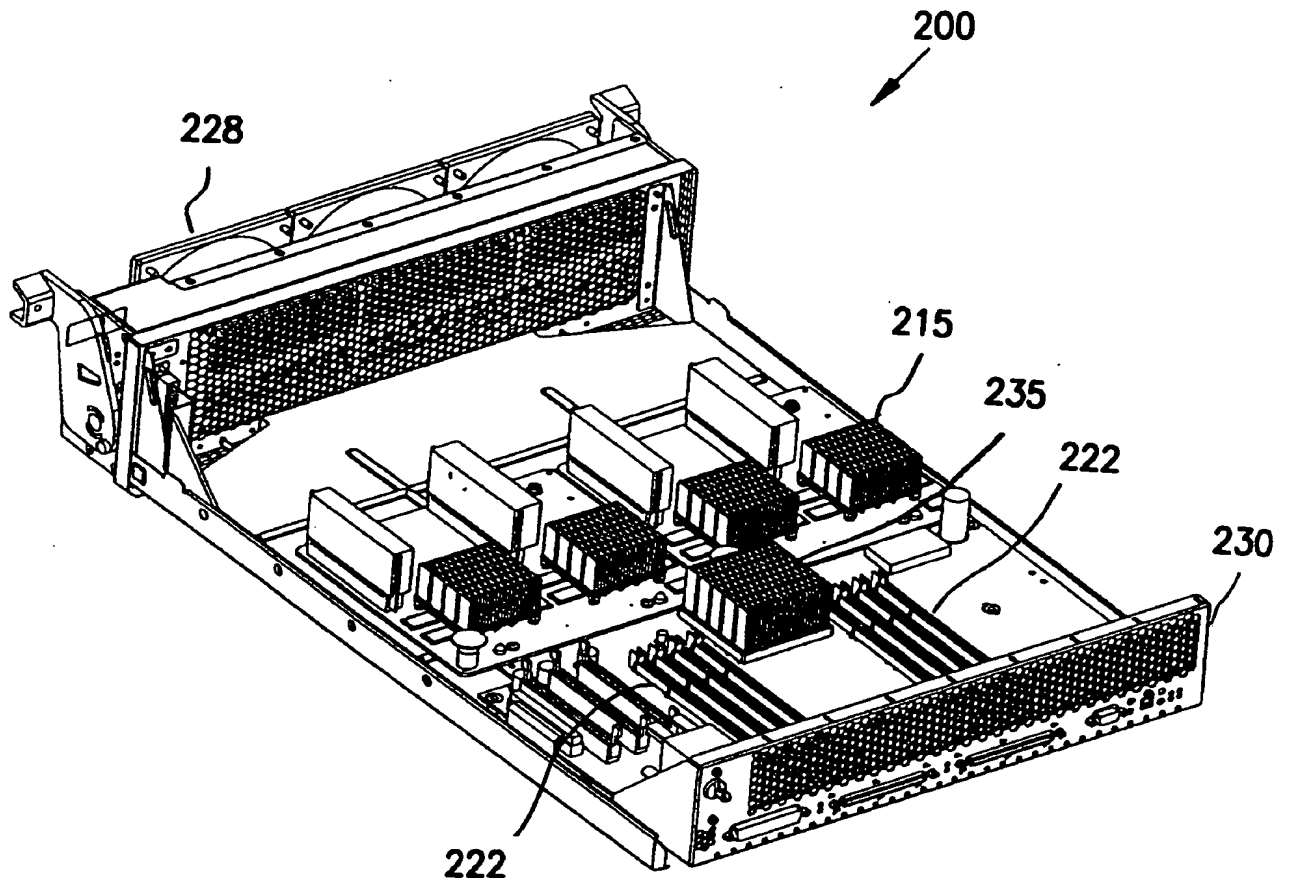


FIG. 2

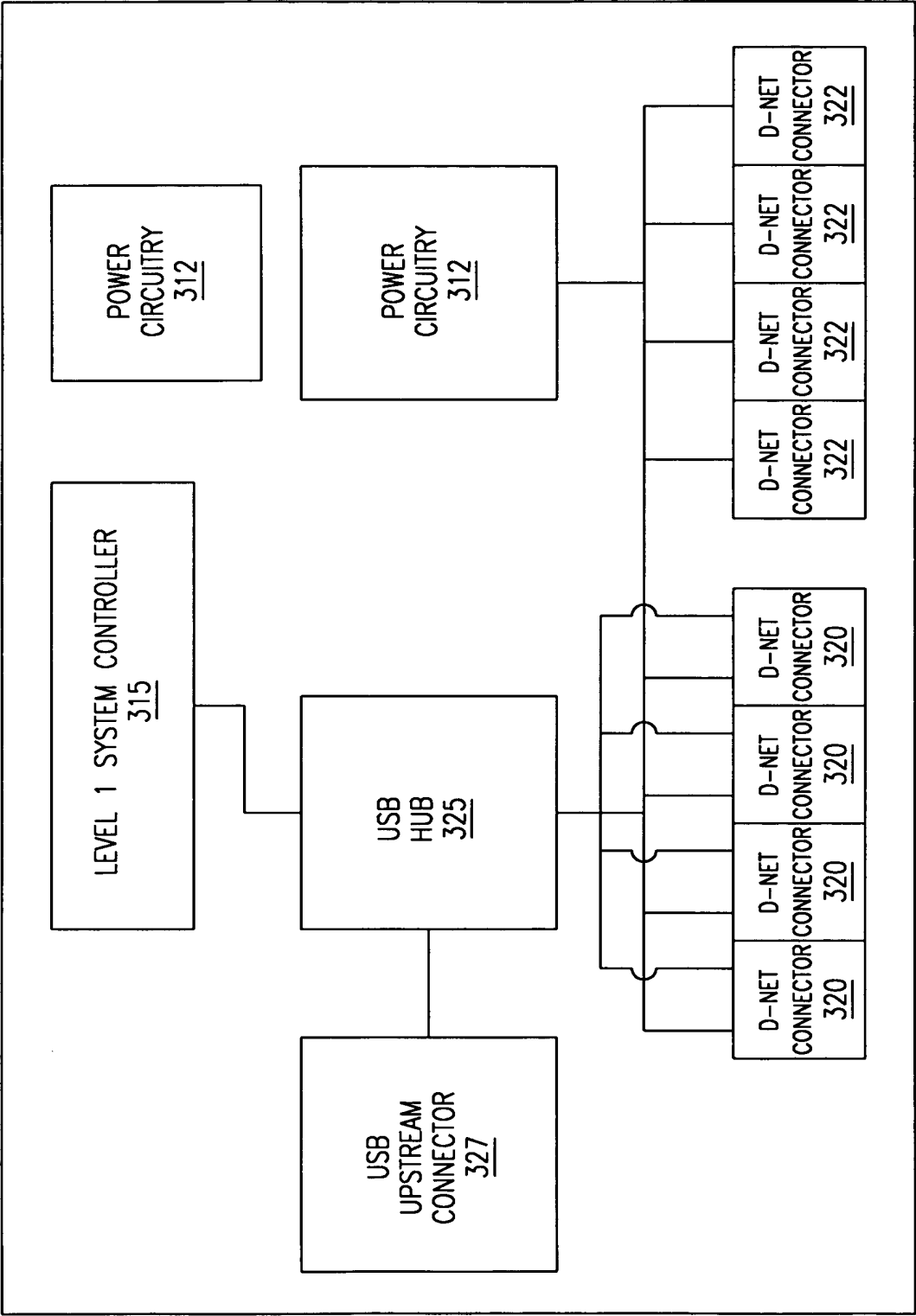
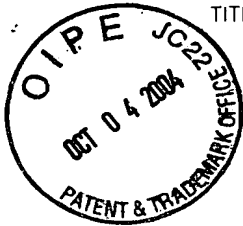


FIG. 3



TITLE: MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE
INVENTOR NAME: Martin M. Deneroff et al.
SERIAL NO.: 09/408,874

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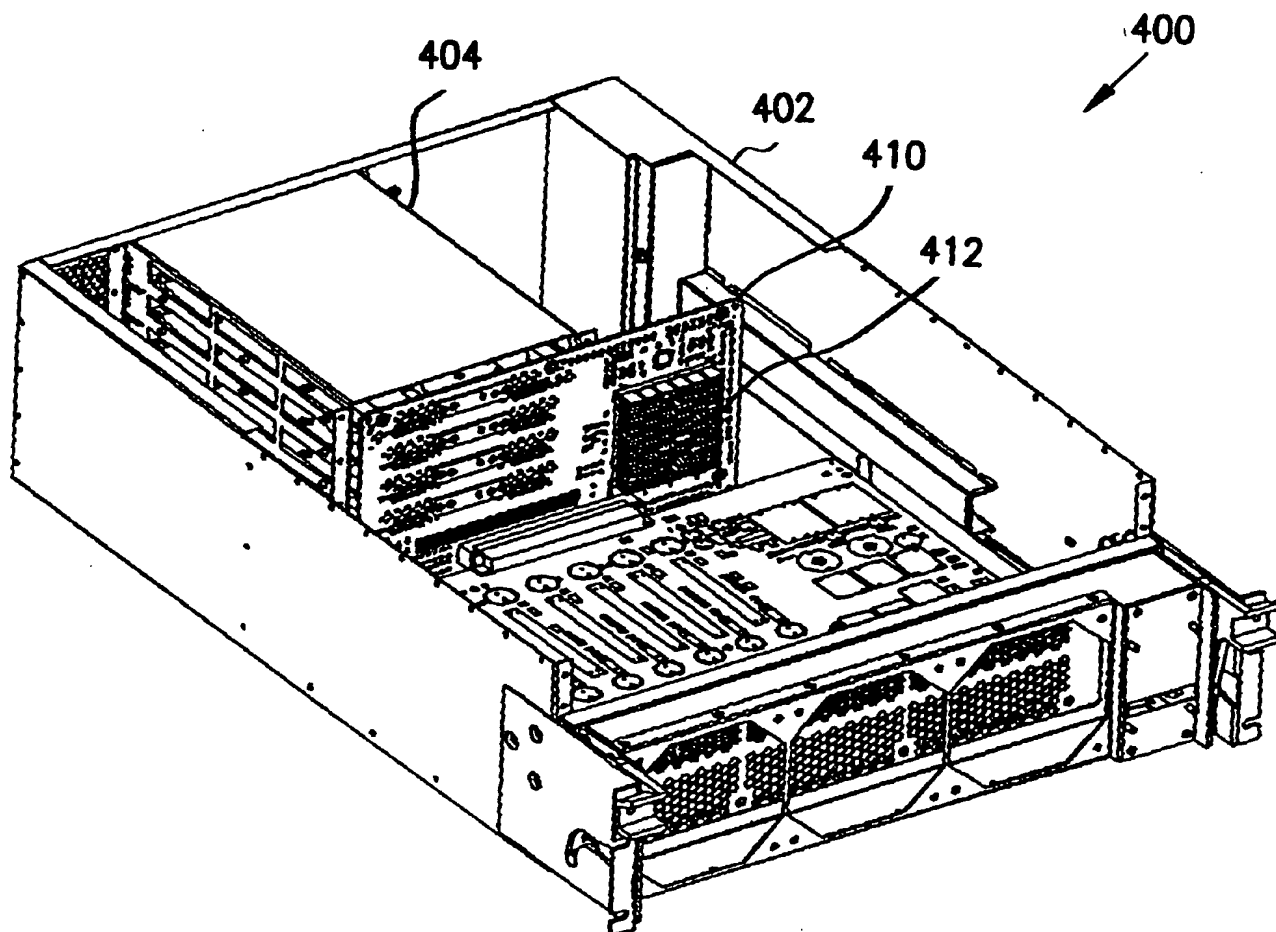
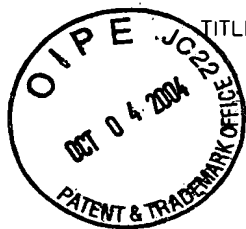


FIG. 4



TITLE: MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION INTERFACE
INVENTOR NAME: Martin M. Deneroff et al.
SERIAL NO.: 09/408,874

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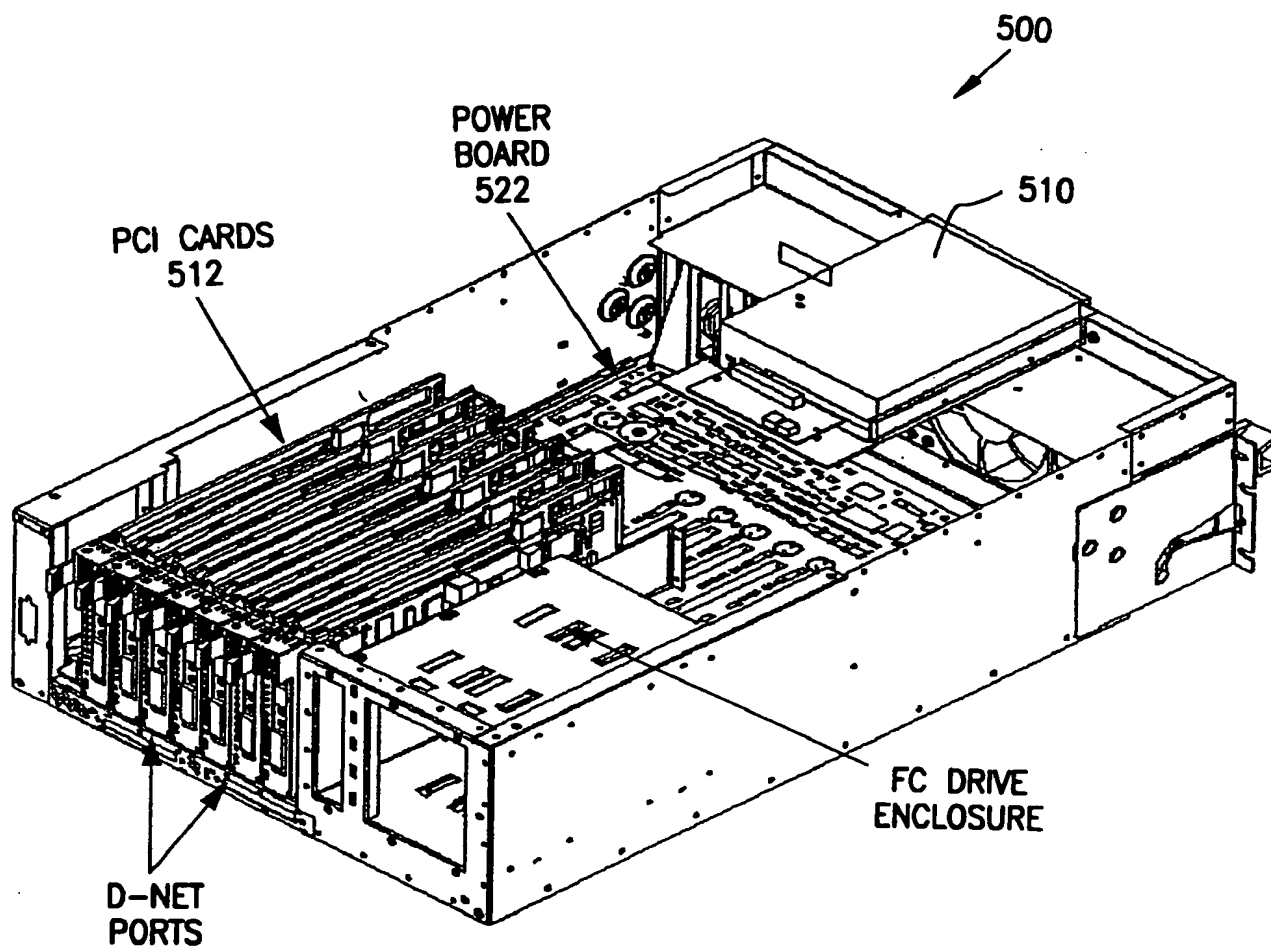


FIG. 5

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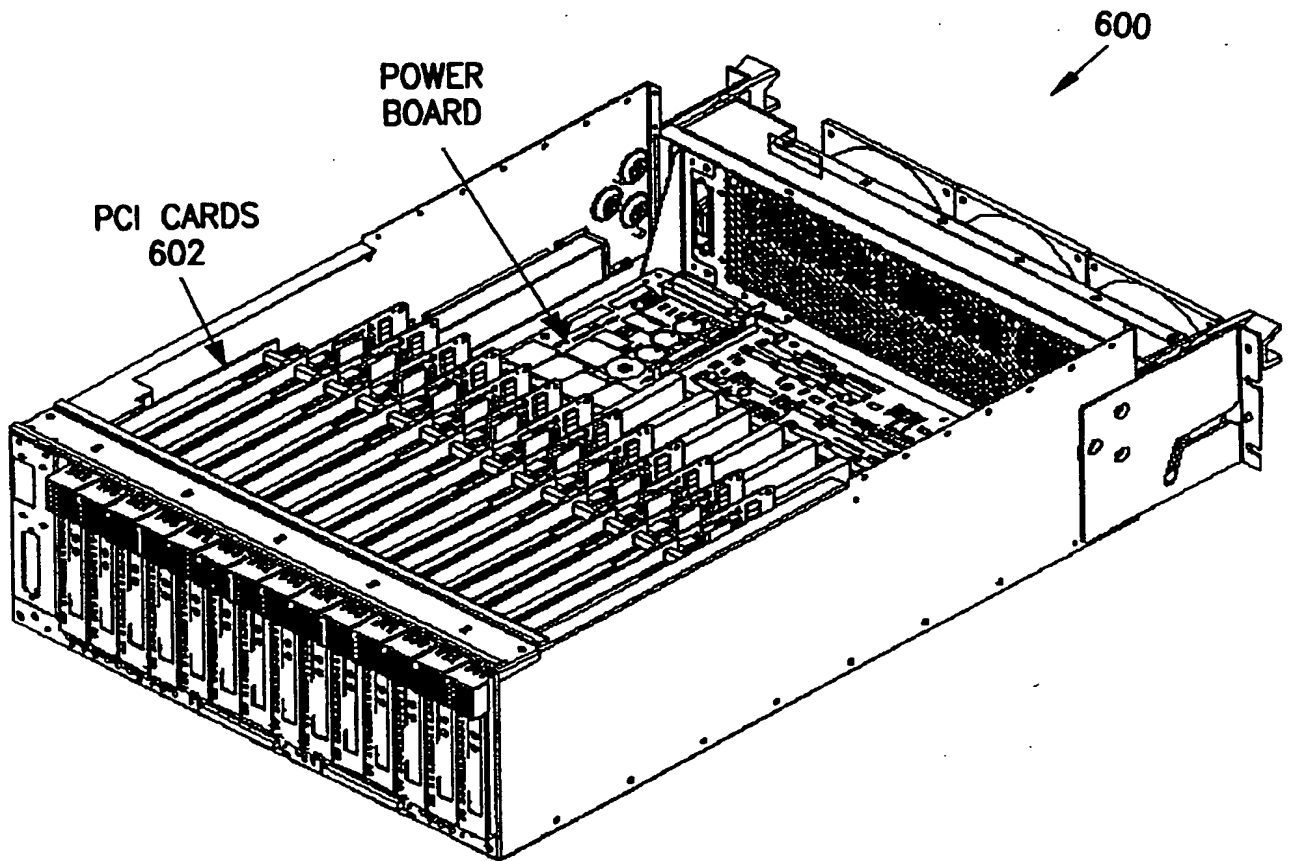
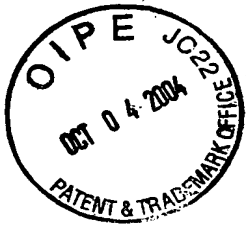
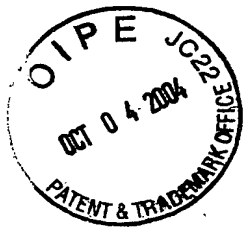


FIG. 6



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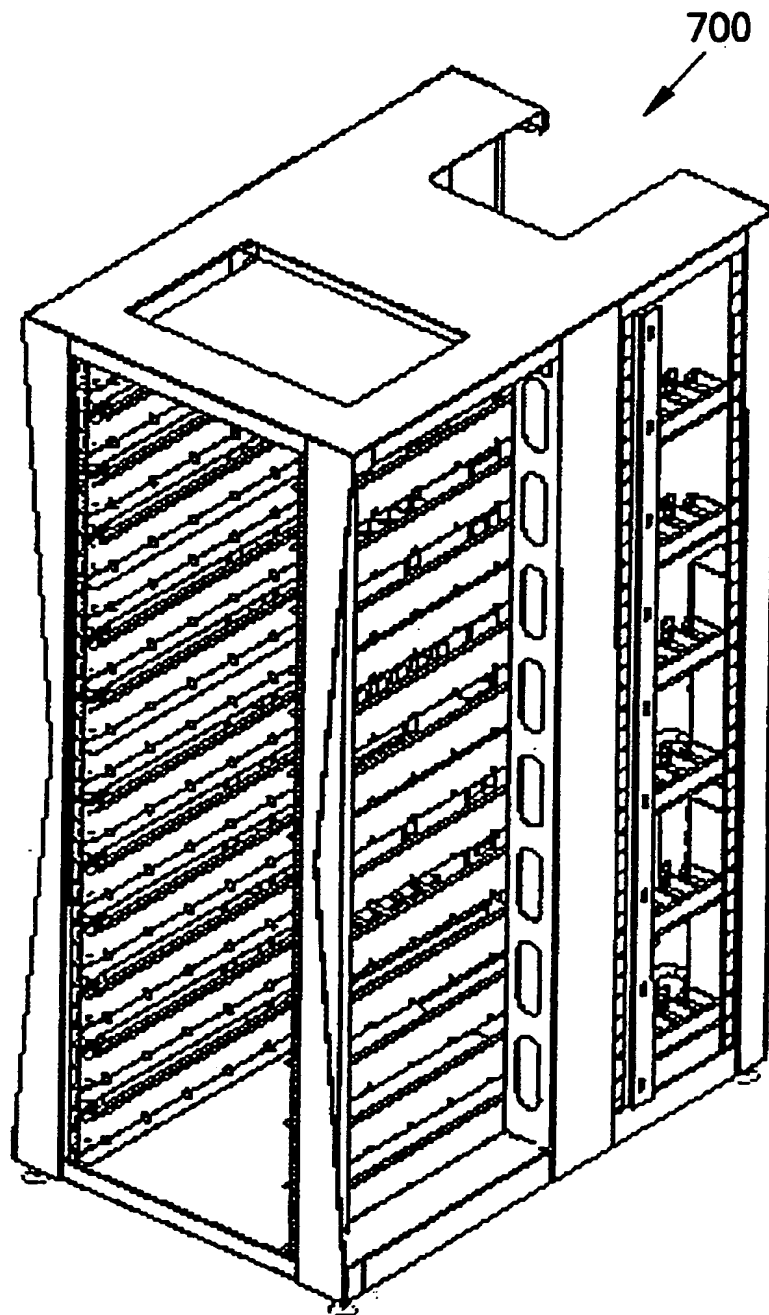


FIG. 7

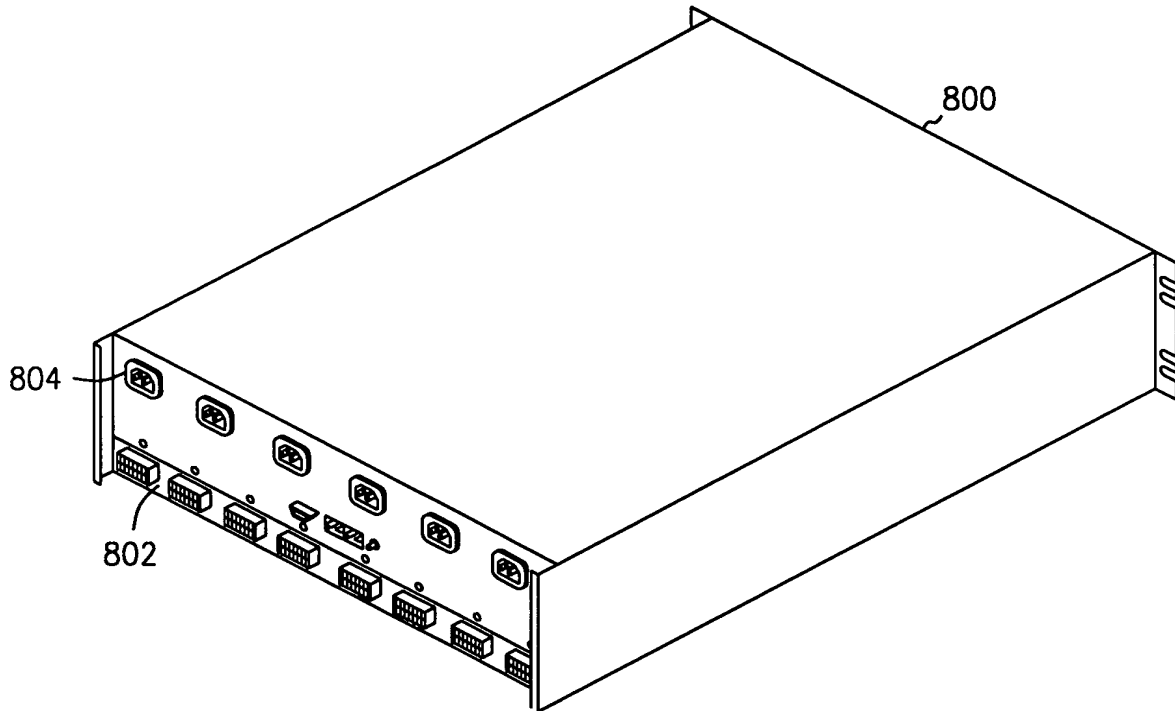


FIG. 8



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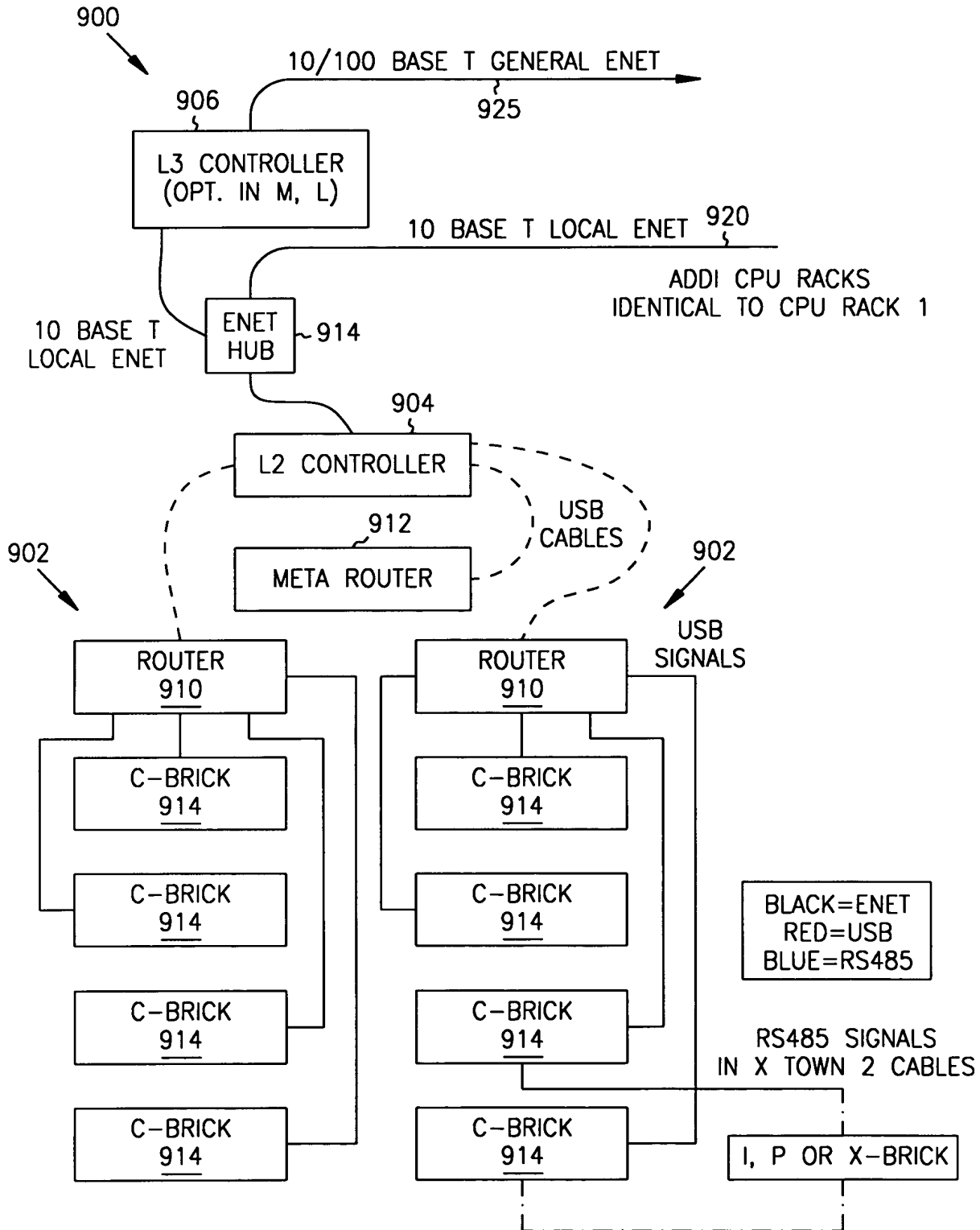


FIG. 9

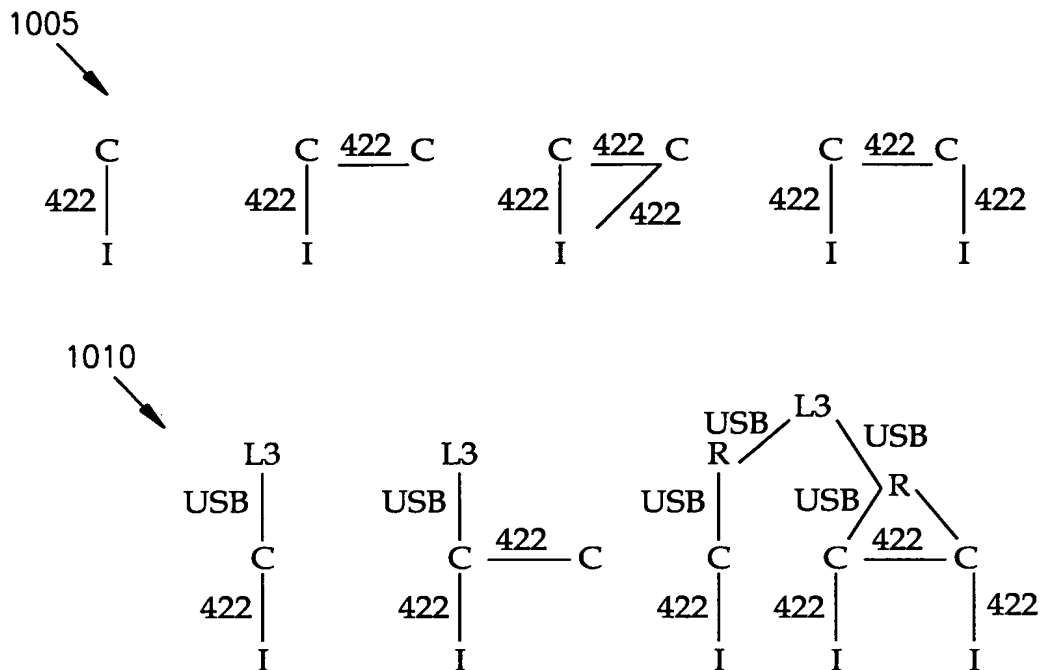


FIG. 10

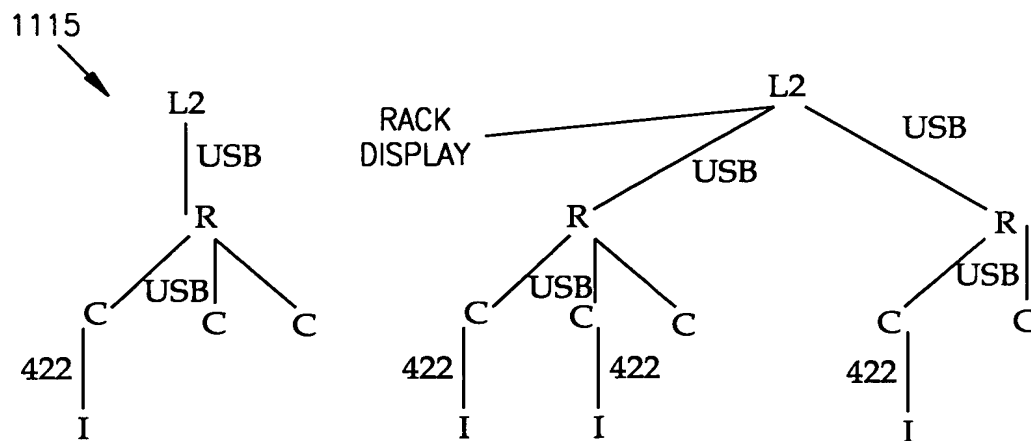


FIG. 11

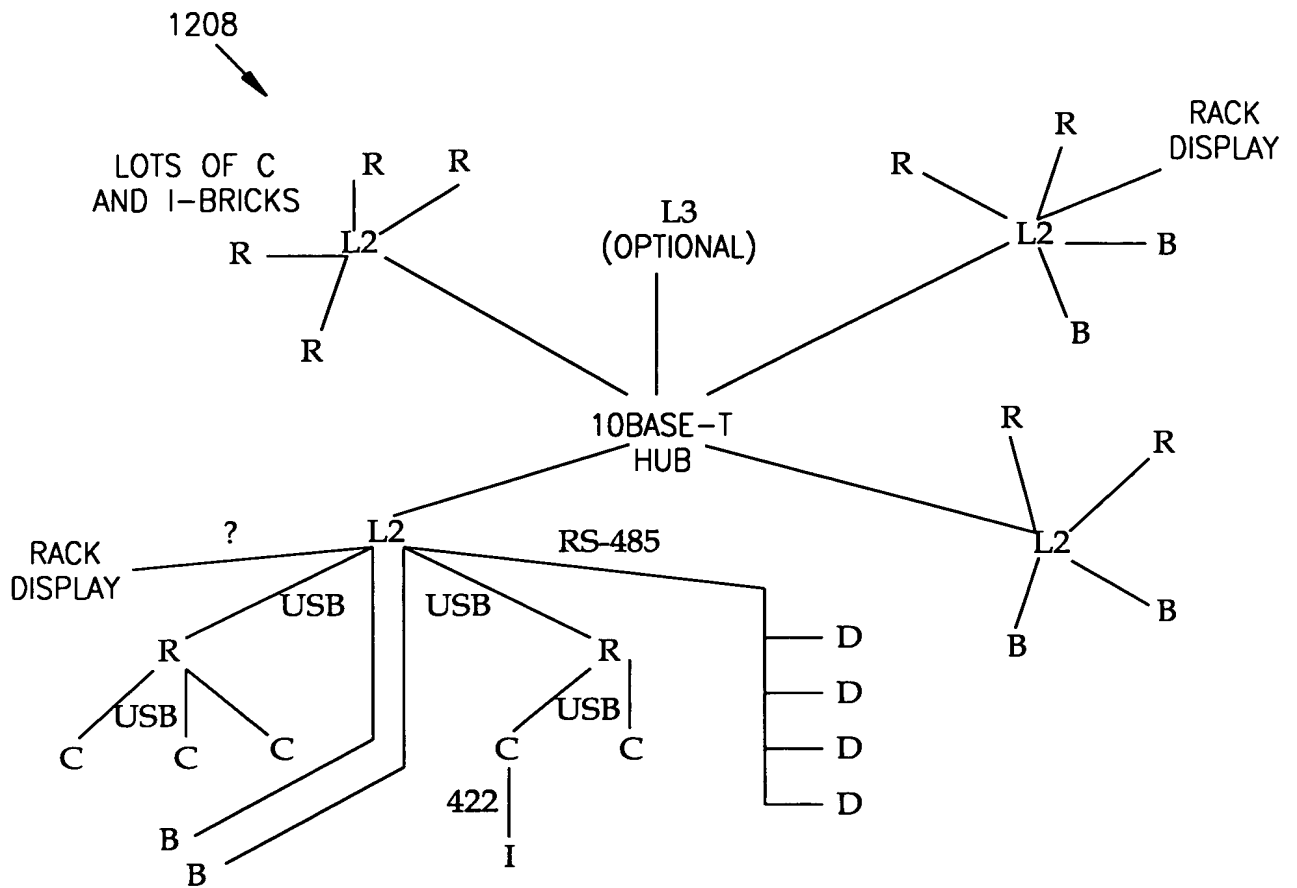


FIG. 12



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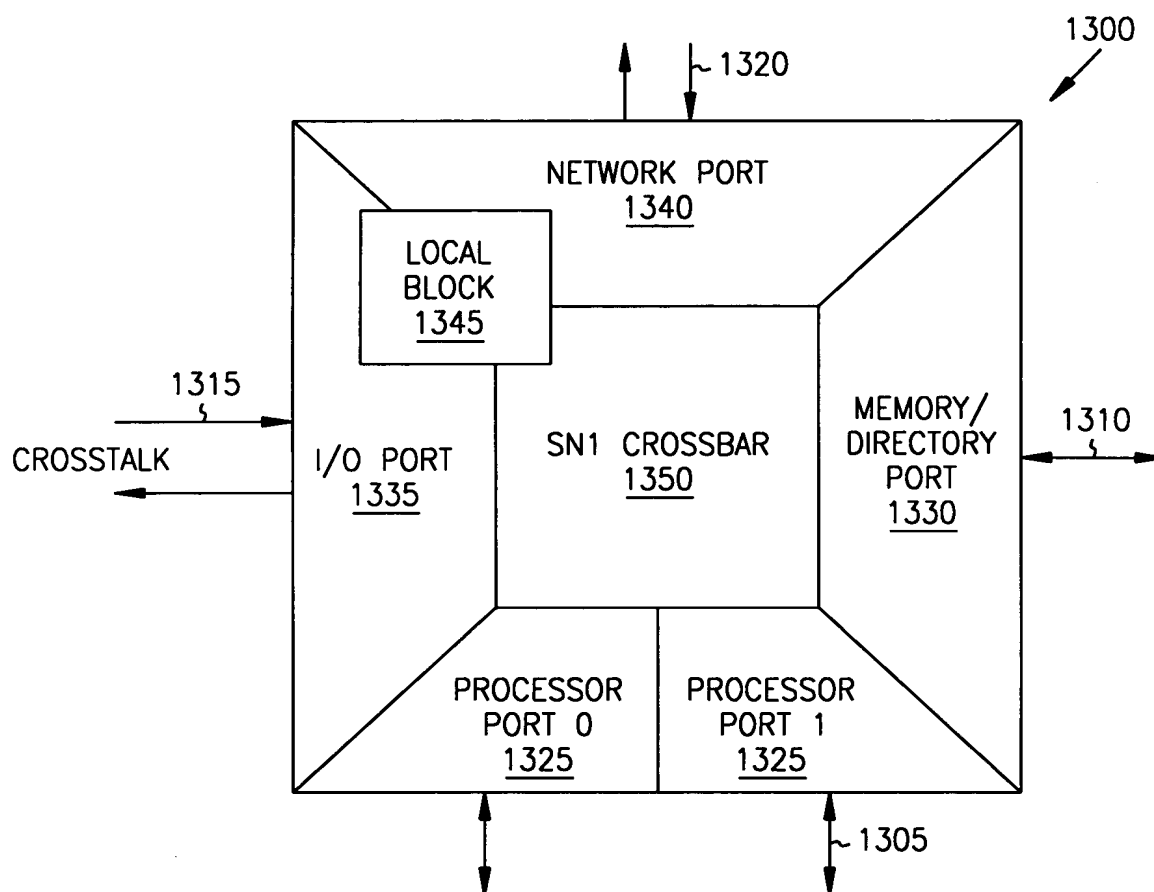


FIG. 13



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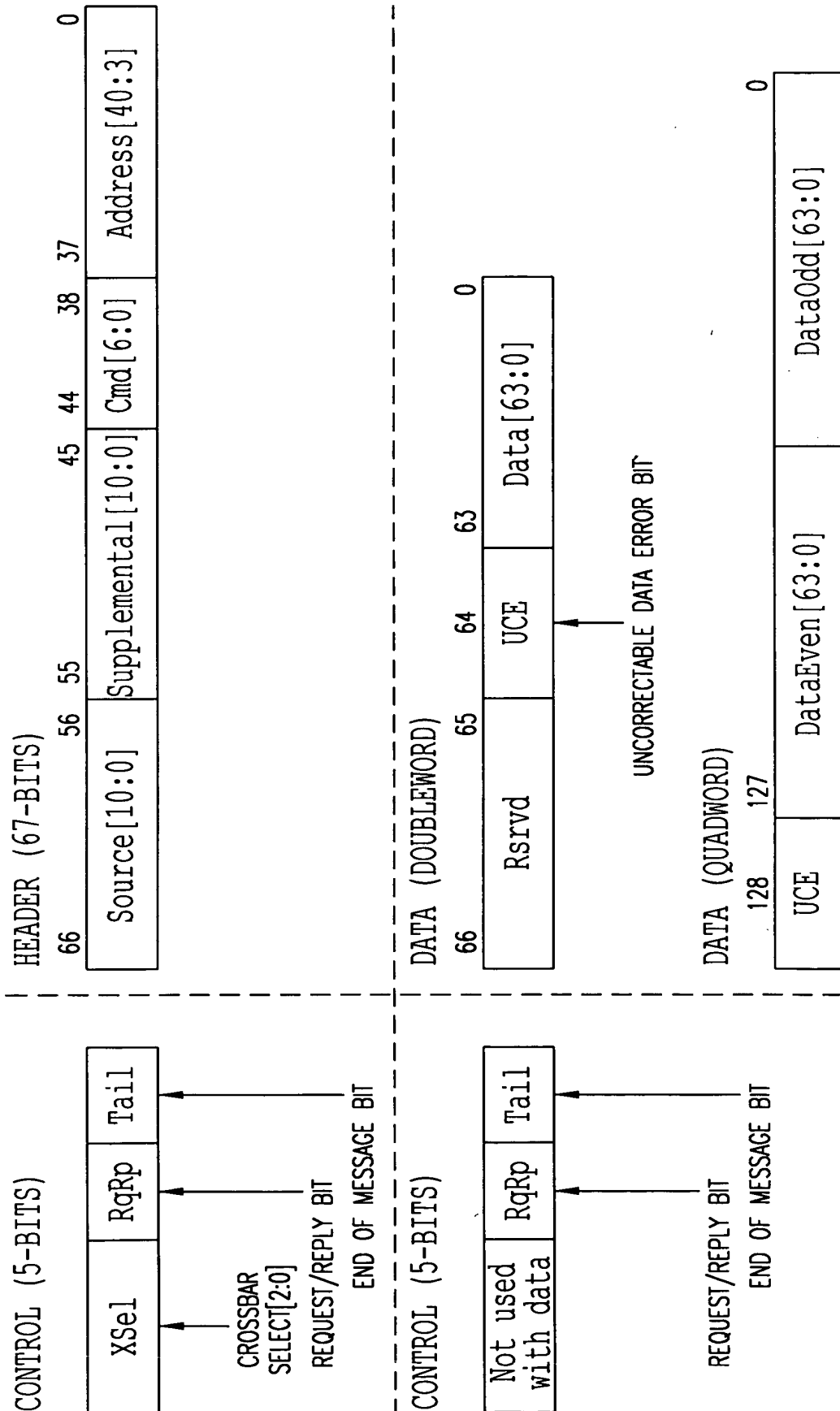


FIG. 14



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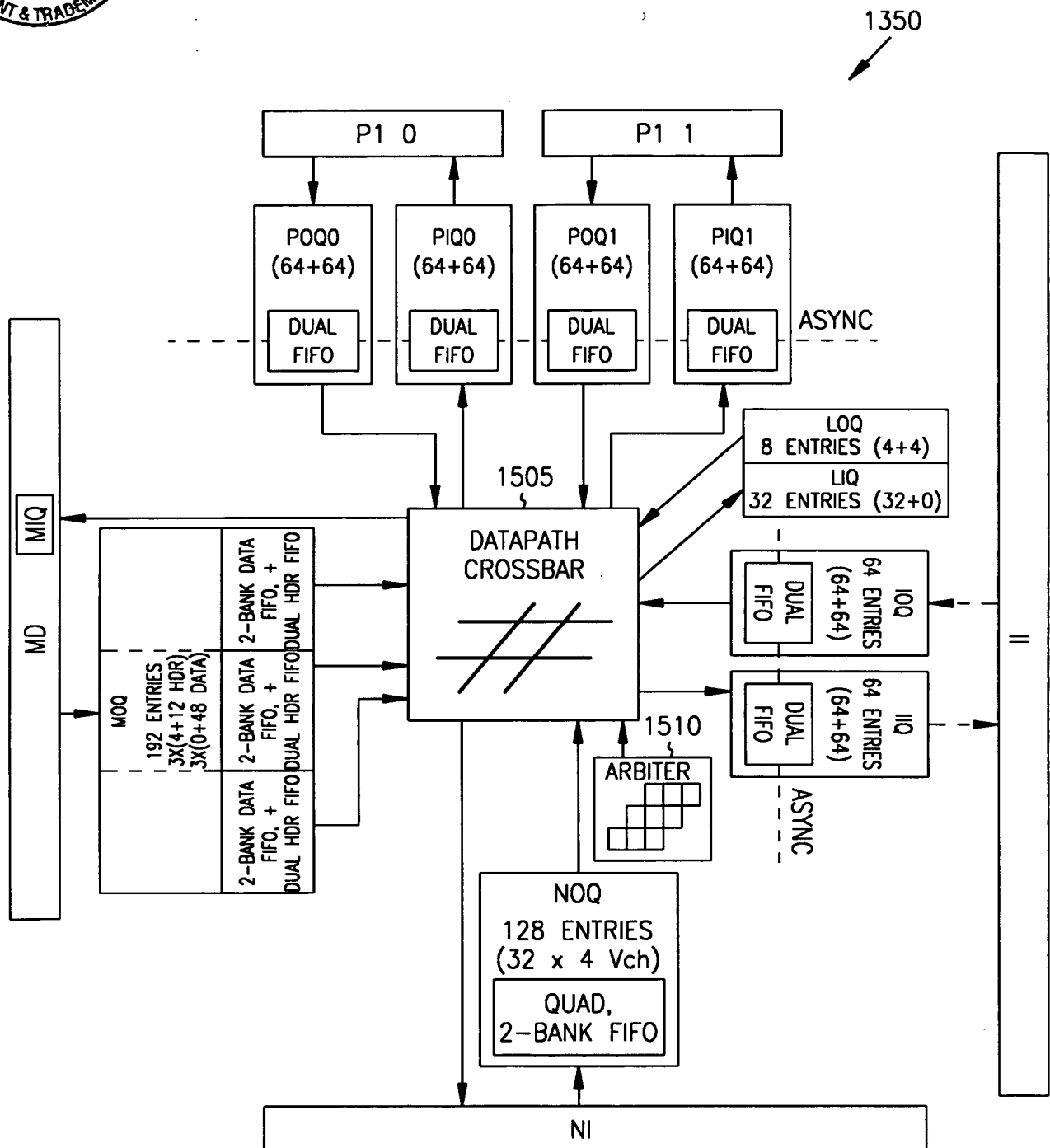


FIG. 15